Hall D Slow Controls Meeting

Date: 11/14/19
Time: 10:00AM – 10:40AM

Attendees: Peter Bonneau, Aaron Brown, Hovanes Egiyan, Brian Eng, Tyler Lemon, Nick Sandoval, Tim Whitlatch, Beni Zihlmann

1. General:
   1.1. Physics run has been delayed; will no longer begin on Nov. 25.

2. Solenoid Status
   2.1. The power supply control board has been replaced with a "known good" board; so far, solenoid status is good.
   2.2. The time change (Daylight Savings Time) has caused issues with the PXI. There is possibly a ROOT function to correct for this issue.

3. BCAL Chillers
   3.1. The communications menu was factory reset for one of the chillers and was most likely the cause for the missing EPICS signals.
       3.1.1. There is a manual for fixing this if settings get changed again
   3.2. The downstream chiller is pegged at 100%
       3.2.1. This is probably not an issue with the compressor cutoff switch; Nick is going to investigate
   3.3. Nick and Tim are going to look into changing the PID Loop for the upstream chiller to fix issues.
   3.4. Despite these issues, BCAL temperatures are stable.

4. ComCAL Installation/Survey
   4.1. ComCAL chiller is off
   4.2. Installation will possibly begin on Monday Nov. 18.
   4.3. The LEDs have been fixed.
   4.4. There are 9 bases that need to be swapped; should be ready by the time installation begins.

5. DIRC Interlocks
   5.1. The DIRC Interlock screen needs to be changed.
       5.1.1. The Liquid Level interlock is now just an alarm. This needs to be reflected on the screen. Also, there is no longer a need for an "interlock override" button.
   5.2. Tile temperatures have been added to the alarm system.
       5.2.1. A reset button may be needed to power cycle the FPGAs
   5.3. After reset, the EPICS IOC needs to be rebooted once everything is powered on

6. Other Business
   6.1. Tyler has fixed the bit order issue for the WEDM GUIs
   6.2. The Target WEDM GUIs have been made and Chris Carlin has been contacted about placing them online.